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What is telephone

interview?Telephonic Interview:A

telephonic interview is a pre-scheduled interview which takes place between the recruiter and candidate over the

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Interview Questions: Johnson, Trey:

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it generates logic 0 and logic 1 when white portion is under sensor. Design Direction finder block using digital components (flip flops and gates) to indicate speed. Logic 0 for clockwise and Logic 1 for counter clockwise. Q Page 4 of 9

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Register-transfer-level abstraction is used in HDL to create high-level representations of a circuit, from which lower-level representations and ultimately actual wiring can be derived. RTL Verilog -

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