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Applications ... Coarse-grained reconfigurable architectures (CGRAs) offer performance and energy efficiency close to those of ASICs and a programmability level closer to application software, offering a good trade-off between NRE and device cost. However, CGRAs have not yet been widely adopted because of a lack of mature architectures and programming tools. Special Issue "Design Tools and Architectures for Coarse ... Reconfigurable computing is a computer architecture combining some of the flexibility of software with the high performance of hardware by processing with very flexible high speed computing fabrics like field-programmable gate arrays. The principal difference when compared to using ordinary microprocessors is the ability to make substantial changes to the datapath itself in addition to the control flow. On the other hand, the main difference from custom hardware, i.e. application-specific integr Reconfigurable computing - Wikipedia Reconfigurable Logic: Architecture, Tools, and Applications offers a snapshot of the state of the art of reconfigurable logic systems. Covering a broad range of architectures, tools, and applications, this book: Explores classical FPGA architectures and their supporting tools Reconfigurable Logic: Architecture, Tools, and ... Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design— the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution. Reconfigurable computers serve as affordable, fast, and accurate tools

for developing designs ranging from single chip architectures to multi-chip and embedded

... Reconfigurable Computing | ScienceDirect In this talk we will discuss the details of reconfigurable computing and FPGA architectures. I will present some of the architectural and CAD tool limitations that reconfigurable computing applications pose and discuss some solutions that I have developed: register-enhanced architectures, pipeline-aware timing-driven placement, and pipeline-aware routing. Candidate Talk: Reconfigurable Computing: Architectural ... In this introduction to reconfigurable computing, we give an overview of the hardware architectures of reconfigurable computing machines, and the software that targets these machines, such as compilation tools. Finally, we consider the issues involved in run-time reconfigurable systems, which re-use the configurable hardware during program execution. An Introduction to Reconfigurable Computing The book can also be used as teaching reference for a graduate course in computer engineering, or as reference to advance electrical and computer engineers. It provides a very strong theoretical and practical background to the field of reconfigurable computing, from the early Estrin's machine to the very modern architecture like coarse-grained reconfigurable device and the embedded logic devices. Introduction to Reconfigurable Computing: Architectures ... Read "Applied Reconfigurable Computing. Architectures, Tools, and Applications 14th International Symposium, ARC 2018, Santorini, Greece, May 2-4, 2018, Proceedings" by available from Rakuten Kobo. This book constitutes the proceedings of the 14th International Conference on Applied

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