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application is a real-time face detection and tracking using FPGA. Face tracking will depend on calculating the centroid of each detected region. A DE2-SoC Altera board has been used to implement this application.

The application based on few algorithms that filter the captured images to detect them. A study of FPGA-based System-on-Chip designs for real-time

... Samsung Electronics Co., Ltd., the world's leader in advanced semiconductor solutions, announced today that it has expanded its industry-leading portfolio of CMOS image sensors to include a new high-definition 1/4-inch, 1.2 Megapixel (Mp) system-on-chip (SoC) imager, the S5K4AW, for notebook and desktop computers. Samsung Offers New PC Camera CMOS

Image Sensor System-on ... Embedded DSP Software Design Using Multicore System-on-a-Chip (SoC) Architectures Robert Oshana, in DSP Software Development Techniques for Embedded and Real-Time Systems, 2006 Tools Support for SoC SoC, and heterogeneous processors in general, require more sophisticated tools support. System on a Chip - an overview | ScienceDirect Topics A multiprocessor system-on-chip for real-time biomedical monitoring and analysis: architectural design space exploration . By I. Al Khatib, F. Poletti, D. Bertozzi, L. Benini, M. Bechara, H. Khalifeh, A. Jantsch and R. Nabiev. Abstract. In this paper we focus on MPSoC architectures for human heart ECG real-time monitoring and analysis. This is

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